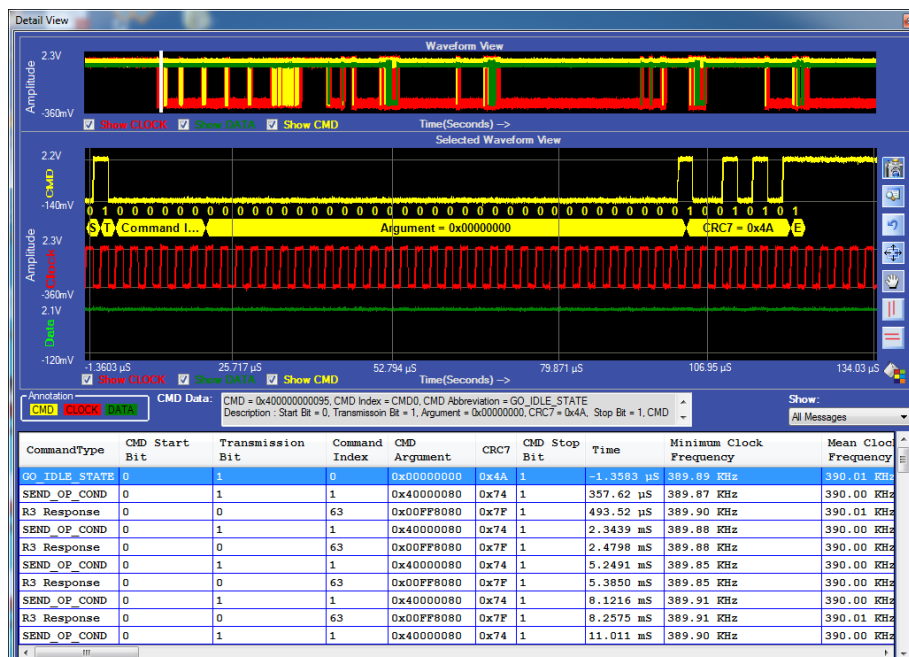


# PGY-MMC-SD Datasheet

## eMMC and SD (UHS-I) Electrical Validation and Protocol Decode Software



Detail View correlates Waveform, Protocol and electrical measurements

### Features

- eMMC and SD (UHS-I) electrical measurements and Protocol testing software conforms to eMMC version 4.51 and SD version 3.01 specification.
- Supports SDR and DDR and Boot mode for electrical measurement and protocol Decode
- eMMC/SD/SDIO Protocol Aware Trigger features
- Software automatically identifies the read and write operations using CMD and apply the electrical parameter limits accordingly.
- Detail View provides efficient debugging capability by correlating the analog waveform, protocol messages and electrical measurements for each protocol packet in single view
- Protocol View lists the protocol activities in sequential form to assist designers to know the host and card transactions
- Time stamp at the end of command token and time stamp at beginning of the response token in Protocol View enables designer to comply with specification and locate any anomaly in timing between host and card
- Software displays the details of command and response in Protocol View and list the errors messages in card status for quick analysis
- Supports cursor based measurements for manual custom measurements
- Ability to store the eMMC and SD protocol data in CSV and txt format
- Utility features like zoom, undo, and fit to screen for easy maneuvering the waveforms while debugging the cause to problem in Detail View makes it easy to use tool
- Software seamlessly integrates with Tektronix windows based oscilloscope and supports live signal analysis using live channels of oscilloscope
- Supports data analysis for 100MB acquisition memory of oscilloscope enables analysis of protocol events for longer duration
- Report generation in pdf format.
- Supports wfm and isf file formats of Tektronix oscilloscope for offline analysis

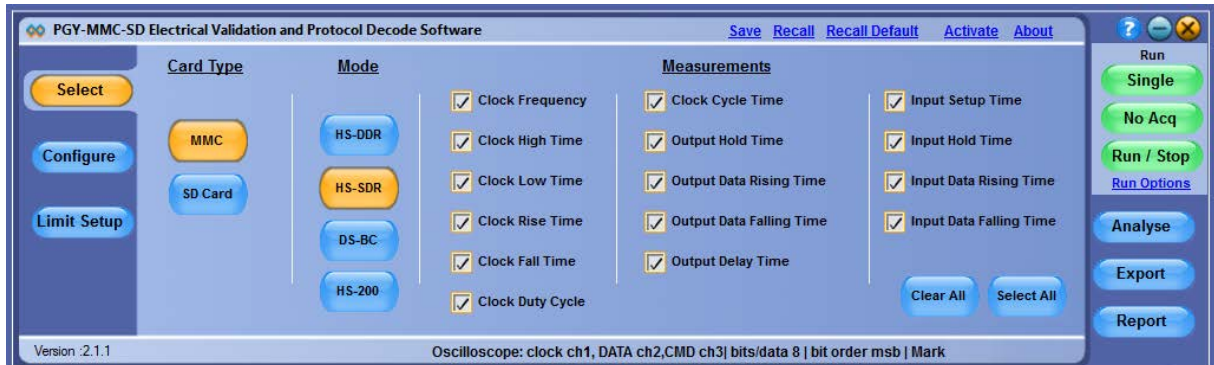
# PGY-MMC-SD Datasheet

## Applications

- ✓ Protocol Analysis
- ✓ eMMC and SD (UHS-I) Electrical Compliance Test
- ✓ Correlation of Analog waveform, Protocol activities and Electrical Measurements

## Seamless Integration with Oscilloscope

PGY- MMC and SD Electrical Validation and Protocol Decode Software runs inside the Tektronix high performance windows oscilloscopes. Automatically imports the data from oscilloscopes live channels. Also supports Tektronix .wfm and .isf file formats. This enable live and offline testing of eMMC and SD Signals.

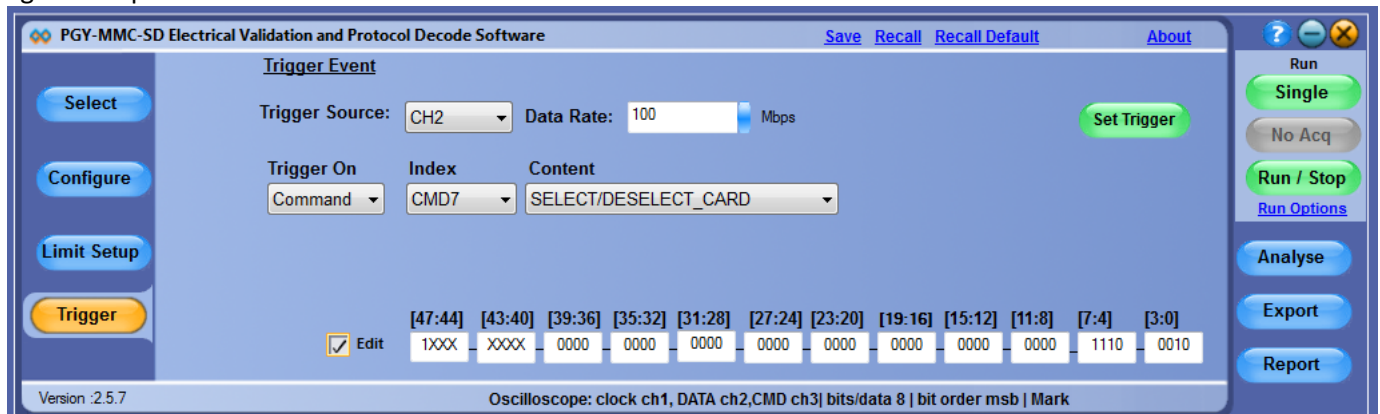


Measurement selections

- Provides the flexibility to select type of Card interface to be tested and related Bus speed modes
- Flexibility select necessary or all electrical measurements
- Save and recall of application setup for repetitive testing at different times
- Supports single and continuous test mode using oscilloscope live data
- Ob-line help

## eMMC/SD/SDIO Protocol Aware Triggering

For efficient test and debugging eMMC/SD/SDIO, it is important to capture signals at right condition. PGY-MMC-SD software provides protocol aware triggering along serial pattern trigger option of the oscilloscope to capture signals at specific event in CMD line.



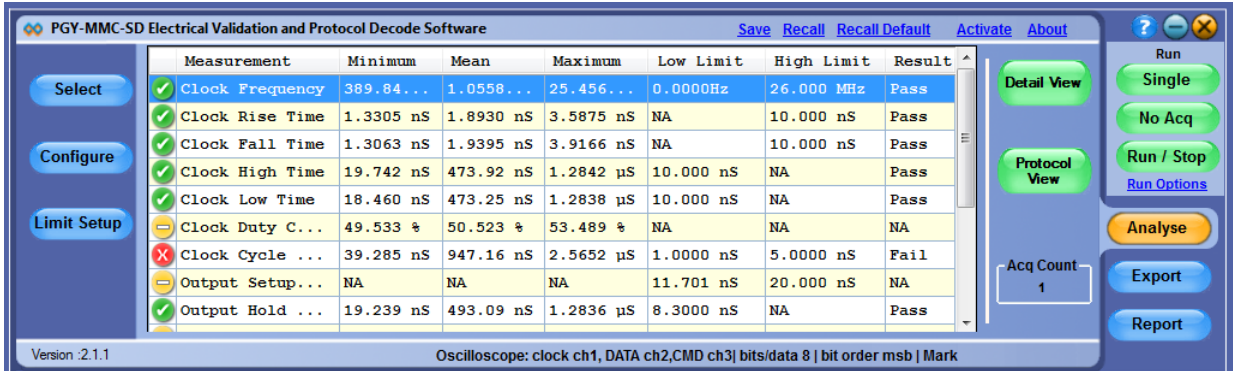
Trigger setup menu

- Flexibility to trigger on command or response
- Offers all the standard triggers patterns with command and Response
- Flexibility to edit trigger pattern

# PGY-MMC-SD Datasheet

## Automated Electrical Validation and Protocol Decode Software

As per the specification of eMMC and SD, the measurement limits are different for read and write operation. The PGY-MMC-SD measurement algorithms automatically find the read and write operations and validates with the respective limits. This enables you to save time in identifying the read and write operation and isolating any compliance issues.



Analyze lists all electrical measurements with pass/fail report

- Lists electrical measurements with mean, minimum and maximum values measured for the entire acquired waveform
- Indicates if measurement exceeds the min or max limits by orange color
- Lower and Upper limits of the electrical measurements are compared against measured values

## Timing Parameters between CMD, Response and Data

eMMC specifies the minimum and maximum cycles to present between the host and device to ensure interoperability. PGY-MMC-SD analyzes the data for these specifications and offers results.

Description	Symbol	Primary Coverage	Minimum	Maximum	Unit	Minimum Measured	Maximum Measured	Results
Data Read Timing	NAC	System	2	10*(TAAC*FOP+100+...	Clock Cycles	12	2260	Pass
Last Host Command to Next Host Co...	NCC	System	8	NA	Clock Cycles	NA	NA	NA
Boot Operation Command - Command...	NCD	System	56	NA	Clock Cycles	92	92	Pass
Boot Operation Command - Data Timi...	NCP	System	74	NA	Clock Cycles	2959	2959	Pass
Assign a Device Relative Address Ti...	NCR	System	2	64	Clock Cycles	5	9	Pass
Device Identification and Device Op...	NID	System	5	5	Clock Cycles	5	5	Pass
Last Device Response to Next Host ...	NRC	System	8	NA	Clock Cycles	131	14003	Pass
R1b Response Timing	NST	System	2	2	Clock Cycles	2	2	Pass
Data Write Timing	NWR	System	2	NA	Clock Cycles	275	18251	Pass
Boot Operation tBA Timing	tBA	System	NA	50	mS	21.096	21.096	Pass

## Protocol View

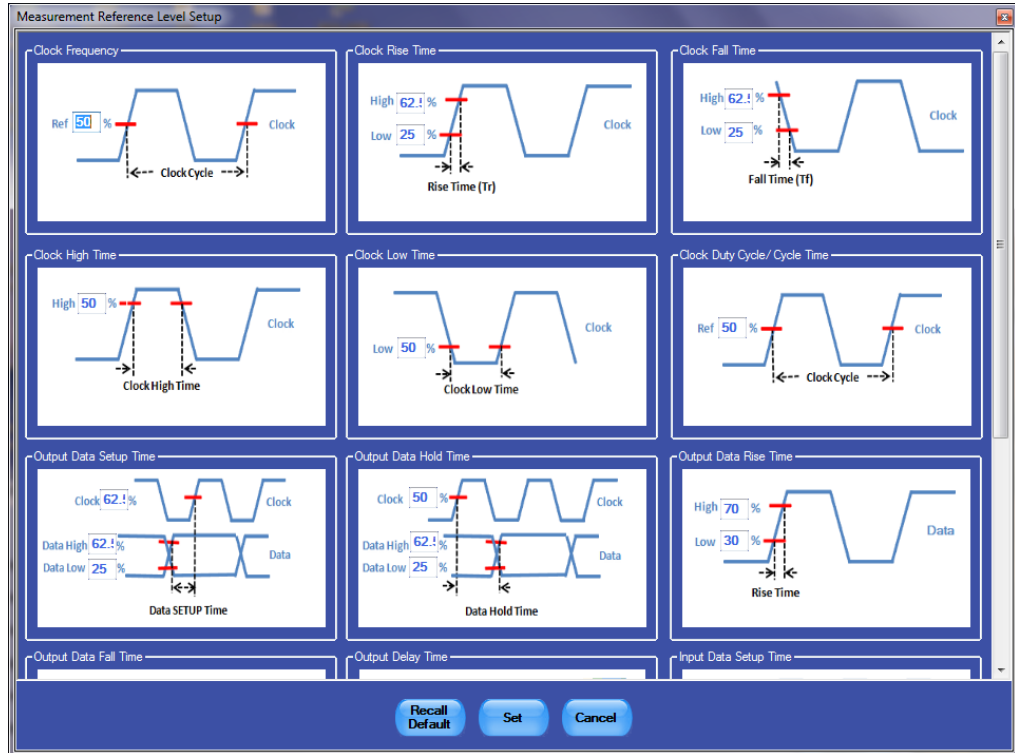
PGY-MMC-SD software lists all the protocol activity between the host and card. Engineers can now quickly view the command and its corresponding response from card. Detail information about the content within command and response is displayed for quick analysis. Selected protocol activity details are listed on right side of the list table. Now Engineers can know the errors reported by card or any other message to host without struggling to know the content of each message.

S#	CMD Index	CMD_Argument	CMD_End Time (TC)	Response Type	Response Check Bits / Index	Response Status	Response Start Time (TR)	Delta (TR-TC)
0	CMD0	0x00000000	121.72 μS	-	-	-	-	-
1	CMD1	0x40000080	480.70 μS	R3	b000001	0x40000080	357.62 μS	0.0000S
3	CMD1	0x40000080	2.4669 mS	R3	b000001	0x40000080	2.3439 mS	12.821 μS
5	CMD1	0x40000080	5.3722 mS	R3	b000001	0x40000080	5.2491 mS	12.821 μS
7	CMD1	0x40000080	8.2446 mS	R3	b000001	0x40000080	8.1216 mS	12.821 μS
9	CMD1	0x40000080	11.134 mS	R3	b000001	0x40000080	11.011 mS	12.821 μS
11	CMD2	0x00000000	14.168 mS	R2	b111111	0x00000000	14.045 mS	12.821 μS
13	CMD3	0x00110000	14.888 mS	R1	b000111	0x00110000	14.765 mS	12.820 μS
15	CMD9	0x00110000	15.486 mS	R2	b111111	0x00110000	15.363 mS	15.385 μS
17	CMD10	0x00110000	16.206 mS	R2	b111111	0x00110000	16.083 mS	15.385 μS
19	CMD7	0x00110000	17.096 mS	R1	b000111	0x00110000	16.973 mS	15.385 μS
21	CMD6	0x03B90000	21.092 mS	R1b	b000110	0x03B90000	20.969 mS	15.385 μS
23	CMD6	0x03B70000	22.827 mS	R1b	b000110	0x03B70000	22.825 mS	20.513 μS
25	CMD16	0x00000200	24.188 mS	R1	b010000	0x00000200	24.186 mS	320.08 nS
27	CMD23	0x00000004	27.653 mS	R1	b010111	0x00000004	27.651 mS	280.09 nS
29	CMD25	0x00000001	28.210 mS	R1	b011001	0x00000001	28.208 mS	280.01 nS
31	CMD16	0x00000200	37.313 mS	R1	b010000	0x00000200	37.311 mS	320.17 nS
33	CMD23	0x00000004	40.785 mS	R1	b010111	0x00000004	40.783 mS	280.09 nS
35	CMD18	0x00000001	41.347 mS	R1	b010010	0x00000001	41.345 mS	279.96 nS
37	CMD6	0x03B90000	60.593 mS	R1b	b000110	0x03B90000	60.591 mS	360.44 nS
39	CMD6	0x03B70100	61.797 mS	R1b	b000110	0x03B70100	61.795 mS	320.28 nS
41	CMD16	0x00000200	62.274 mS	R1	b010000	0x00000200	62.272 mS	320.72 nS

Protocol View

## Characterization of PHY layer by custom limit setup

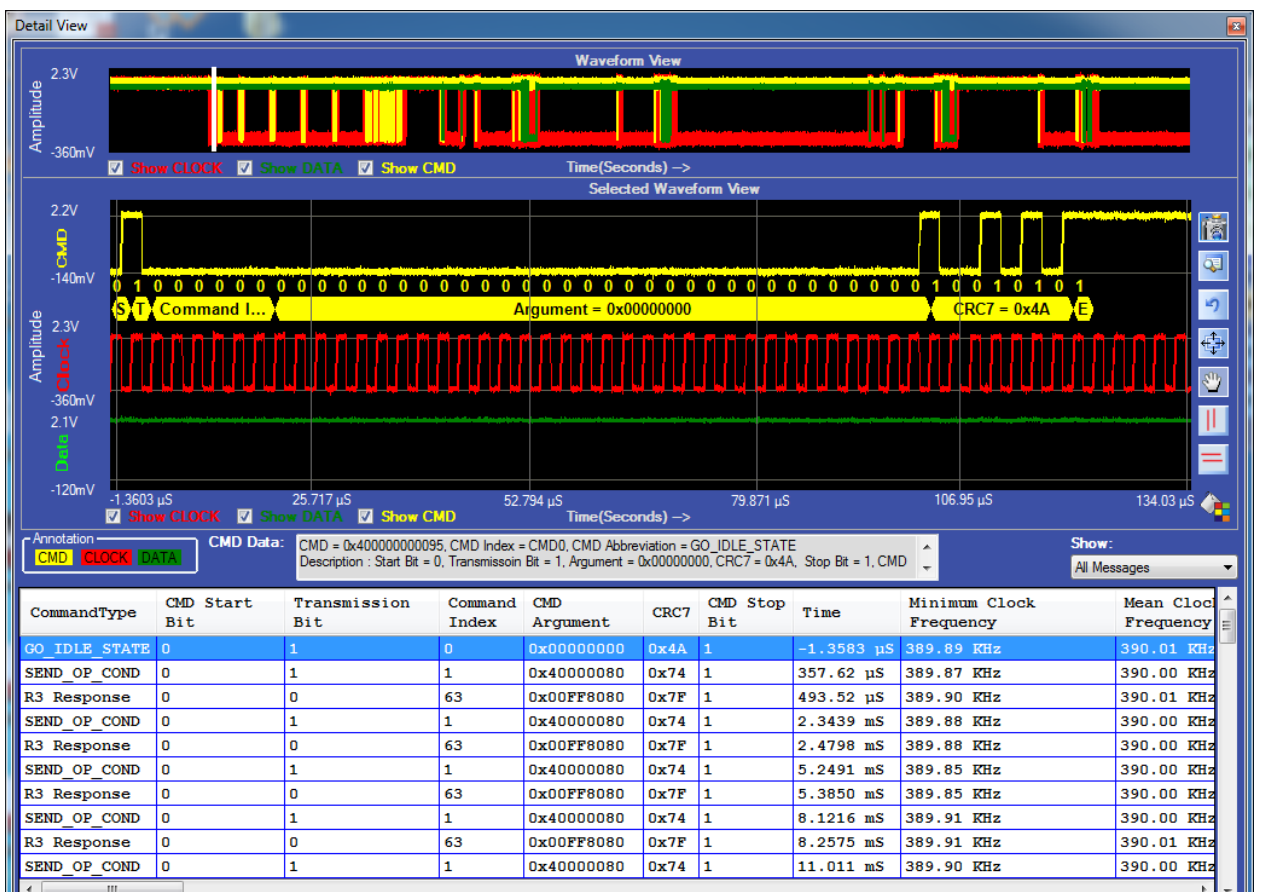
PGY-MMC-SD is not just for standard electrical compliance testing, you can also vary the limits and test your device with custom limits. The intuitive limits and reference level setup allows you to configure the limits and reference levels for your custom testing needs. This enables you to test your device beyond the specification and characterize it.



Config panel to custom set the reference setting for electrical measurement

## Powerful Debug environment: Detail view

In Detail View, engineers can view the analog waveform, details of protocol and electrical measurements in single view. If there is any failure in electrical measurement or error in protocol messages, designers can quickly correlate the protocol data with analog waveforms. These protocol errors can be caused due to the failure in electrical measurements. User can select any row in the detail view; corresponding analog waveform will be zoomed and displayed. In the same row, engineers can view all the electrical measurements corresponding to the selected row. Utility features such as zoom, cursors, and markers make custom measurement while debugging.



Detail view

Detail view provides following capabilities:

- Plots the acquired waveform in waveform view window
- Lists all decoded command and response tokens in each row in decode table
- Identifies type of command and response for easy protocol interpretation
- Lists respective electrical measurements for command and response for each row
- Selected Protocol command or response's related analog waveform is plotted in a window.
- Bus Diagram view overlays protocol data for the selected row along with waveform
- Snap button enables storing selected waveform window for report generation purpose
- Zoom, fit to screen, pan, undo, vertical and horizontal cursors enables quick analysis and measurement of electrical signals

#### Electrical measurements for eMMC and SD Bus

Bus Modes	Electrical Measurements	
eMMC-HS DDR Measurements	Clock Frequency	Clock Low Time
	Clock Rise Time	Clock Fall Time
	Clock Duty Cycle	Output Data Rising Time
	Output Data Falling Time	Output Delay Time
	Input Setup Time	Input Hold Time
	Input Data Rising Time	Input data Falling Time
eMMC-HS SDR Measurements	Clock frequency	Clock Low Time
	Clock Rise Time	Clock Fall time
	Output Data Falling Time	Output Data Rising Time
	Input Setup Time	Output Delay Time
	Input Data Falling Time	
eMMC HS-BC Measurements	Clock frequency	Clock Low Time
	Clock Rise Time	Clock Fall time
	Clock Duty Cycle	Output Setup Time
	Output Hold Time	output Delay Time
	Input Setup time	Input Hold time
eMMC HS-200 Measurements	Clock Period	Data read Setup time
	Clock Rise Time	Data read hold time
	Clock Fall Time	Command output Delay
	Clock Duty Cycle	Data Write output Delay
	Response Setuptime	Command Valid Window
	Response Hold Time	Data Write Valid Window
SD-DDR Measurements	Clock Frequency	Clock Low Time
	Clock Rise Time	Clock Fall Time
	Clock Duty Cycle	Output Setup Time
	Output Delay Time	Input Hold time
	Input Setup time	
Bus Speed	Supports all Data speed; Limited by oscilloscope Bandwidth	
Protocol Decode	Protocol View and Detail View	
Waveforms Plots	Analog signals plotted with Bus diagram in for Protocol format for correlation of PHY information to Protocol data	
Report generation	PDF format report generation	
Export of data	Export to CSV and TXT format	
Oscilloscope file format	WFM and ISF file format of Tektronix oscilloscope	

# PGY-MMC-SD Datasheet

## Tektronix Oscilloscopes Supported

- DPO/MSO5000 series
- DPO7000 series
- DPO/MSO/DSA 70000 series

## Ordering Information

**PGY-MMC-SD** (shipment includes CD with PGY-MMC-SD Electrical Validation and Protocol Decode Software)

## Contact:

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## About Prodigy Technovations Pvt Ltd

Prodigy Technovations Pvt Ltd ([www.prodigytechno.com](http://www.prodigytechno.com)) is a leading global technology provider of Protocol Decode, and Physical layer testing solutions on test and measurement equipment. The company's ongoing efforts include successful implementation of innovative and comprehensive protocol decode and physical Layer testing solutions that span the serial data, telecommunications, automotive, and defense electronics sectors worldwide.

## Other products

- HDMI and MHL Protocol Compliance Test Software
- UniPRO and LLI Protocol Decode Software
- UFS Protocol Decode Software
- HSI Electrical Validation and Protocol Decode Software
- I2C Electrical validation and Protocol Decode Software
- SPI Electrical Validation and Protocol Decode Software
- I2S Electrical, Audio, and Protocol Testing Software
- UART/RS232 Protocol Decode Solution
- FlexRay Protocol and SI Analysis Software
- USB2.0 Protocol Decode Software
- HSIC Protocol Decode Software
- RFFE Protocol Analysis Software
- KX/KR Ethernet DME and Line Training Analysis Software