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Discover Comprehensive I3C Solutions

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I3C Exerciser & Analyzer

- Master (Controller) and/or Slave (target) Support
- Amplitude & Timing Variation: 1V to 3.3V in steps of 30mV
- Protocol Error Injection
- Hot Join & In-Band Interrupt
- Hardware-based Protocol Aware Trigger

I3C Exerciser & Analyzer-LITE

- Master or Slave Emulation of I3C
- System Design and Validation
- Fixed Amplitude (1V, 2V, 1.8V, 2.5V, 3.3V)

I3C Electrical Validation & Protocol Decode

- Post Silicon Physical Validation
- I3C-CTS
- API Support Python & C++
- Electrical Validation of I3C Specification

I3C USB Adapter

- Comprehensive Production Test Solution
- Fixed Voltage Level for Reliable Testing
- Seamless Integration

Logic Analyzer

- Hardware-Based Protocol Aware Trigger
- System Design Support
- Cost-Effective Debugging Solution

Key Features	PGY-I3C-EX-PD	PGY-I3C-EX-PD Lite	PGY-I3C-EV-PD	PGY-I3C-USB-ADT	PGY-LA-EMBD
I3C Protocol Exerciser and Analyzer from 1Hz to 12.5MHz (SDR, HDR, TSL, TSP encoding)	Yes (Beyond I3C Spec)	Yes (1KHz to 12.5MHz as per I3C Spec)	Yes	Yes	only Analyzer
Master (Controller) and/either Slave Support (Target)	Yes	Yes	NA	Yes	Yes
Amplitude and Timing Variation	Yes (1V to 3.3V in steps of 30mV)	Fixed Amplitude (1V, 1.2V,1.8V, 2.5V, 3.3V)	NA	Fixed to one voltage level	NA
Protocol Error Injection	Yes	No	NA	No	NA
Hot Join and In-Band Interrupt Support	Yes	Yes	NA	No	NA
Hardware based Protocol Aware Trigger	Yes	Yes	No	No	Yes
I3C Conformance Test Suite Support (I3C-CTS)	Yes	No	No	No	NA
API Support	Yes (In Python and C++)	Optional	Yes	Yes	No
Electrical validation of I3C Specification	No	No	Yes	No	No
Applications	Post Silicon Protocol Validation, Pre Silicon Protocol Traffic Generation for analysis in Emulation and Prototype Design Stage	Emulation of I3C Master or Slave, Application Engineering team, System Design and Validation	Post Silicon Physical Layer Validation	Production Test Solution	System Design
Price	X	0.5X	0.3X	0.2X	0.2X
Value Proposition	Performance Solution	Firmware development and Field Application	Physical layer Characterization	Production	Debug Solution